

<u>जौ</u>

x

THE



# NOAA

#### **Accelerating Research-to-Operations**

Neil Jacobs, Ph.D.

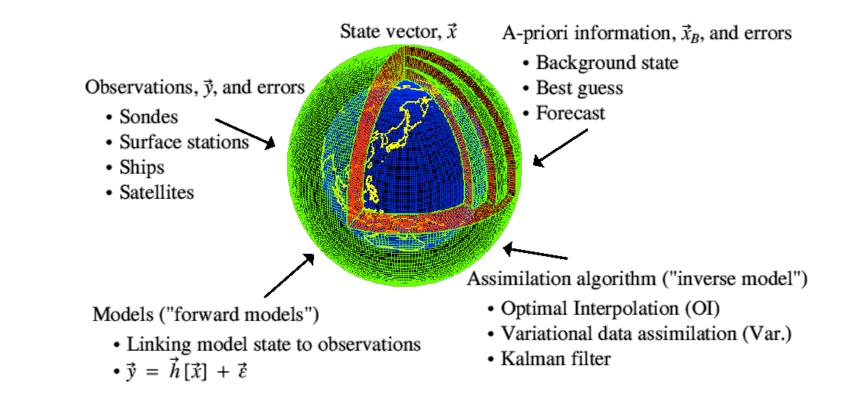
Assistant Secretary of Commerce for Environmental Observation and Prediction Deputy NOAA Administrator

July 17, 2018



# **Numerical weather prediction**

#### NWP = f (observations + model code + compute)





ž

. .

K

LOC

22

# Improving observing systems





ž

# **Observations are not created equal**

15

10

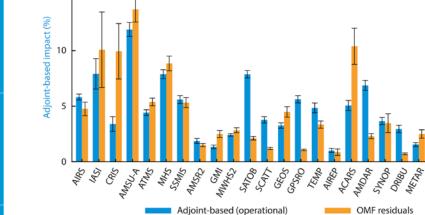
5

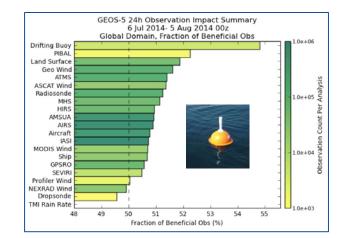
PILOT SHIP NUMHS

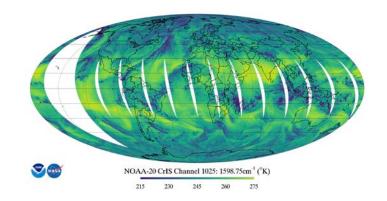
OMF residuals impact (%)

- **Observing system experiments**
- **Forecast sensitivity to observations**
- **Cost-benefit analysis / value proposition**
- **Alternative observations / CWDP**













R

Dot

12

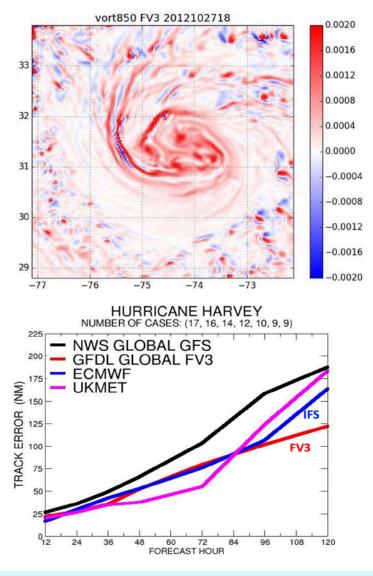
15

ž

# Improving forecast skill and performance

- Quality control of observations
- Data assimilation
- Dynamic core and model physics
- NGGPS SIP for a Unified Forecasting System
- Code efficiency
- Optimized hardware







ž

<u>त्रौ</u>

R

DOD

12

#### Inherent barriers with the status quo

- Fractured strategy (getting much better!)
  - Obtuse HPC procurement process
- Security clearance procedures for visiting scientists
- Cultural (internal and external)
- Funding allocation process disincentivizes collaboration
- Risk aversion (incentive not to fail > incentive to improve)
- Too many committees with overlapping and conflicting input



żźś

औ

K

#### Ideas to accelerate research-to-operations

- Virtual machines (cloud HPC) for on-demand parallel development
- End-to-end community model (harness collective advancements)
- Visiting scientists (UCP/PrepIFS)
- Modifying the R2O funnel (requirements, gates and transitions)
- Fast-tracking satellite DA
- Drive up benefit in cost-benefit ratio
- Agile/nimble "skunkworks" sandbox
- Governance / funding



żź

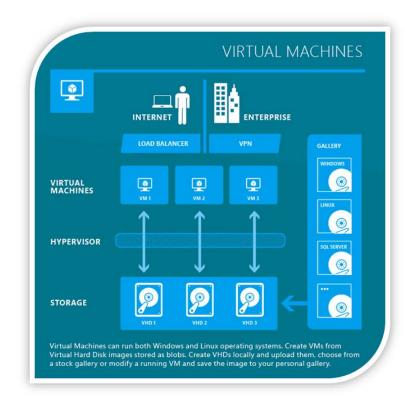
औ

R

## VMs and cloud HPC

- Virtual machines (cloud HPC) for parallel *community* development
- Not all HPC "clouds" are the same
- Consistent with NOAA BDP
- Remote Direct Memory Access (RDMA)

   Remote memory location read/write
   No copying
   Direct processor interface bypasses
   kernel and TCP/IP in I/O path
- Consistent with NOAA BDP
- Secure ingest (DMZ)





ž

त्री

K

# Thank you for serving in this role!

Your feedback and guidance is critical; we are looking to you.

- What do you think are our biggest challenges?
- Thoughts on how to solve them?
- Alternative ideas to our proposed path?
- Are we overlooking something?
- Unintended consequences?
- What do you need from us?
- How can we better engage SAB?



ž

औ